GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN AUGUST 2020
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3201 P R No : 201909073 Sex : F Name : EESHA POONJA A
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AO P	
	IA	2	AO P	
Digital Signal Processors & Embedded Systems				
	Theory	4	AO P	
	IA	2	AO P	
Design for Testability & E-Waste Management				
	Theory	4	AA P	
	IA	2	AA P	
Processor Architecture & Parallel Processing				
	Theory	4	AO P	
	IA	2	AO P	
Memory Design				
	Theory	4	AO P	
	IA	2	AO P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	AA P	
FPGA & Embedded Systems Lab				
	IA	2	AA P	
	Practical	2	AA P	
	Total :	38		9.63 P PASSES

Seat No : 3202 P R No : 201909074 Sex : F Name : NAIK SHAMA SHASHIDHAR
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AO P	
	IA	2	AO P	
Digital Signal Processors & Embedded Systems				
	Theory	4	AA P	
	IA	2	AA P	
Design for Testability & E-Waste Management				
	Theory	4	AB P	
	IA	2	AB P	
Processor Architecture & Parallel Processing				
	Theory	4	AO P	
	IA	2	AO P	
Memory Design				
	Theory	4	AA P	
	IA	2	AA P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	AA P	
FPGA & Embedded Systems Lab				
	IA	2	AA P	
	Practical	2	AB P	
	Total :	38		9.11 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3203 P R No : 201105295 Sex : M Name : NAVTI PRANAV NARAYAN
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BC	P	
	IA	2	BC	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	BB	P	
Processor Architecture & Parallel Processing					
	Theory	4	AB	P	
	IA	2	AB	P	
Memory Design					
	Theory	4	AA	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	BB	P	
	Practical	2	BB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
	Total :	38			7.74 P
					PASSES

Seat No : 3204 P R No : 201911225 Sex : M Name : SATYAM MADHUSUDAN ACHARI
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AB	P	
	IA	2	AB	P	
Design for Testability & E-Waste Management					
	Theory	4	AO	P	
	IA	2	AO	P	
Processor Architecture & Parallel Processing					
	Theory	4	AA	P	
	IA	2	AA	P	
Memory Design					
	Theory	4	AA	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	BB	P	
	Total :	38			8.74 P
					PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3205 P R No : 201401673 Sex : F Name : SHARVA SHIVANAND SALELKER
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AA	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	AB	P	
	IA	2	AB	P	
Processor Architecture & Parallel Processing					
	Theory	4	AA	P	
	IA	2	AA	P	
Memory Design					
	Theory	4	AA	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AA	P	
FPGA & Embedded Systems Lab					
	IA	2	AA	P	
	Practical	2	AA	P	
	Total :	38			8.79 P
					PASSES

Seat No : 3206 P R No : 201510511 Sex : M Name : SINAI KARAPURKAR AMOGH SANJEEV
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AA	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	AA	P	
	IA	2	AA	P	
Processor Architecture & Parallel Processing					
	Theory	4	AA	P	
	IA	2	AA	P	
Memory Design					
	Theory	4	AA	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AA	P	
	Practical	2	AA	P	
	Total :	38			8.89 P
					PASSES

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Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3207 P R No : 201510514 Sex : F Name : SINGBAL NIDHI NANDAN
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	AO P	
	IA	2	AO P	
Digital Signal Processors & Embedded Systems				
	Theory	4	AO P	
	IA	2	AO P	
Design for Testability & E-Waste Management				
	Theory	4	AO P	
	IA	2	AO P	
Processor Architecture & Parallel Processing				
	Theory	4	AA P	
	IA	2	AA P	
Memory Design				
	Theory	4	AO P	
	IA	2	AO P	
Parallel Processing Lab				
	IA	2	AA P	
	Practical	2	AA P	
FPGA & Embedded Systems Lab				
	IA	2	AA P	
	Practical	2	AA P	
	Total :	38		9.63 P

PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By :
Checked By :
Date : 21/12/2020

Assistant Registrar-E(Proff.) Assistant Registrar Exam Controller Of Examinations

GOA UNIVERSITY TALEIGAO PLATEAU GOA

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